

Serial No. 10/662,209

Application Filing Date: September 12, 2003

Amendment and Response to Restriction Requirement of May 17, 2007
Filed June 8, 2007

Amendment to the Claims:

This listing of claims will replace all prior versions and listing of the claims in the application:

Listing of Claims:

1. (Original) A video surveillance system comprising:
at least two video cameras each configured to independently generate video data;
and
a video controller coupled with the video cameras, wherein the video controller is configured to substantially synchronize and then merge the video data generated by each of the video cameras to form a single contiguous stream of common video data,
the single contiguous stream of common video data storable in a data file.
2. (Original) The video surveillance system of claim 1, wherein the video controller is configured to direct the video cameras to independently generate video data that is generated substantially in phase with a phase relationship that remains constant.
3. (Original) The video surveillance system of claim 1, further comprising a camera clock configured to generate a common clock signal, wherein the video cameras are enabled to generate video data with the same common clock signal.
4. (Original) The video surveillance system of claim 1, wherein the single contiguous stream of common video data is storable by the video controller in a continuous loop such that the oldest video data is overwritten by the newest video data.
5. (Original) The video surveillance system of claim 1, wherein the single contiguous stream of common video data comprises a plurality of frames of video data from each of the video cameras that alternate between each of the video cameras on a frame-by-frame basis.

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6. (Original) The video surveillance system of claim 1, wherein the video controller is configured to interleave frames of video data from each of the video cameras to form the single contiguous stream of common video data.

7. (Original) A video surveillance system comprising:
at least two video cameras each configured to independently generate video data;
and
a video controller coupled with the video cameras, wherein the video controller is configured to direct substantially synchronized generation of the video data in a constant phase relationship by each of the video cameras,
the video controller further configured to merge the video data generated by each of the video cameras to form a single contiguous stream of common video data,
the single contiguous stream of common video data storable in a data file.

8. (Original) The video surveillance system of claim 7, wherein the single contiguous stream of common video data is representative of the video data generated by each of the video cameras.

9. – 11. (Canceled)

12. (Currently Amended) The video surveillance system of claim [[10]] 21, wherein the shock sensor comprises a detector and a housing, wherein the detector is disposed within the housing without contacting the housing, the indication to the ~~video controller~~ microcontroller is in response to a force that causes contact between the housing and the detector.

13. (Original) The video surveillance system of claim 7, wherein the video controller comprises a portable memory device that is detachable from the video controller, the single contiguous stream of common video data storable in the portable memory device as the data file.

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14. (Original) The video surveillance system of claim 13, wherein the portable memory device is a FLASH memory card.

15. (Original) A video surveillance system, the video surveillance system comprising:

a first video camera configured to independently generate a first stream of video data;

a second video camera configured to independently generate a second stream of video;

a sync and frame merge module coupled with the first and second video cameras, wherein the sync and frame merge module is configured to enable generation of the second stream of video data in substantial synchronization with generation of the first stream of video data by establishment of a constant phase relationship between the first and second streams of video data,

the sync and frame merge module also configured to switch between the first and second streams of video data on a frame-by-frame basis to generate a single contiguous stream of common video data;

a video processing module coupled with the sync and frame merge module, wherein the video processing module is configured to compress the single contiguous stream of common video data; and

a microcontroller coupled with the video processing module, wherein the microcontroller is configured to direct storage of the compressed single contiguous stream of common video data.

16. (Original) The video surveillance system of claim 15, further comprising a memory device detachably coupled with the microcontroller, wherein the memory device comprises a FLASH memory configured to store the single contiguous stream of common video data.

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17. (Original) The video surveillance system of claim 15, wherein the microcontroller directs the storage of a predetermined amount of the single contiguous stream of video data in a continuous loop.

18. (Original) The video surveillance system of claim 17, wherein the video data comprises a plurality of first video frames generated by the first video camera and a plurality of second video frames generated by the second video camera, wherein the single contiguous stream of video data comprises a portion of the first video frames interleaved between a portion of the second video frames.

19. (Original) The video surveillance system of claim 15, further comprising a buffer coupled with the microcontroller and the video processing module, wherein the buffer is configured to temporarily store the single contiguous stream of common video data until the microcontroller directs storage of the single contiguous stream of common video data.

20. (Original) The video surveillance system of claim 15, further comprising a power conditioning module coupled with the microcontroller, the power conditioning module configured to indicate low supply voltage conditions to the microcontroller and maintain the supply voltage to the microcontroller above the low supply voltage condition for a determined period of time, the microcontroller configured to perform an orderly shutdown of the video surveillance system in response to indication from the power conditioning module of low supply voltage conditions.

21. (Original) The video surveillance system of claim 15, further comprising a shock sensor coupled with the microcontroller, wherein the microcontroller is configured to cease storage of the compressed single contiguous stream of common video data a determined amount of time after forces above a determined threshold are indicated by the shock sensor.

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22. (Original) The video surveillance system of claim 15, wherein the constant phase relationship between the first and second streams of video data comprises one of a determined phase offset and in phase.

23. (Original) A video surveillance system comprising:

a first video camera configured to independently generate a first stream of video data;

a second video camera configured to independently generate a second stream of video data;

a camera clock coupled with the first video camera, the camera clock configured to provide a common clock signal to the first video camera to enable generation of the first stream of video data; and

a clock hold off circuit coupled with the second video camera and the camera clock, wherein the clock hold off circuit is configured to selectively enable the second video camera with the common clock signal to generate the second stream of video data in substantial synchronization with generation of the first stream of video data.

24. (Original) The video surveillance system of claim 23, further comprising a video data merger circuit coupled with the first and second video cameras, the video data merger circuit configured to merge the first and second streams of video data to form a contiguous stream of common video data.

25. (Original) The video surveillance system of claim 24, further comprising a video processing module coupled with the video data merger circuit, wherein the video processing module is configured to decode the contiguous stream of common video data into a digital form and compress the digital form of the contiguous stream of common video data to minimize data storage requirements.

26. (Original) The video surveillance system of claim 24, further comprising a video processing module coupled with the video data merger circuit, wherein the video processing

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module is configured to compress the contiguous stream of common video data to minimize data storage requirements.

27. (Currently Amended) The video surveillance system of claim [[23]] 15, wherein the first and second video cameras are configured to independently generate the respective first and second streams of video data in analog form.

28. (Currently Amended) The video surveillance system of claim [[23]] 15, wherein the first and second video cameras are configured to generate the respective first and second streams of video data in digital form.

29.-35. (Canceled)